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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): HIMI et al.

Application No.: 10/716,606

Filed: September 28, 2003

Title: METHOD FOR MANUFACTURING
SEMICONDUCTOR

Group Art Unit: 2813

Examiner: PHAM, THANHHA S

Attorney Docket No.: 01-100-DIV

Commissioner for Patents
Alexandria, VA 22314

November 3, 2005

RESPONSE TO RESTRICTION/ELECTION REQUIREMENT

Sir:

In response to the Election/Restrictions requirement mailed on October 6, 2005 in connection with the above application, Applicants hereby elect Invention I (claims 1 – 5 and 15 – 20; drawn to a method of making a semiconductor device) and Embodiment I of Fig. 1 with traverse. Applicants state that claims 1 – 4 read on the elected Embodiment I.

Examination of the present application in view of the above election is respectfully requested.

Please charge any necessary fees to Deposit Account 50-1147.

Respectfully submitted,

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